

FIG. 1A

FIG. 1B

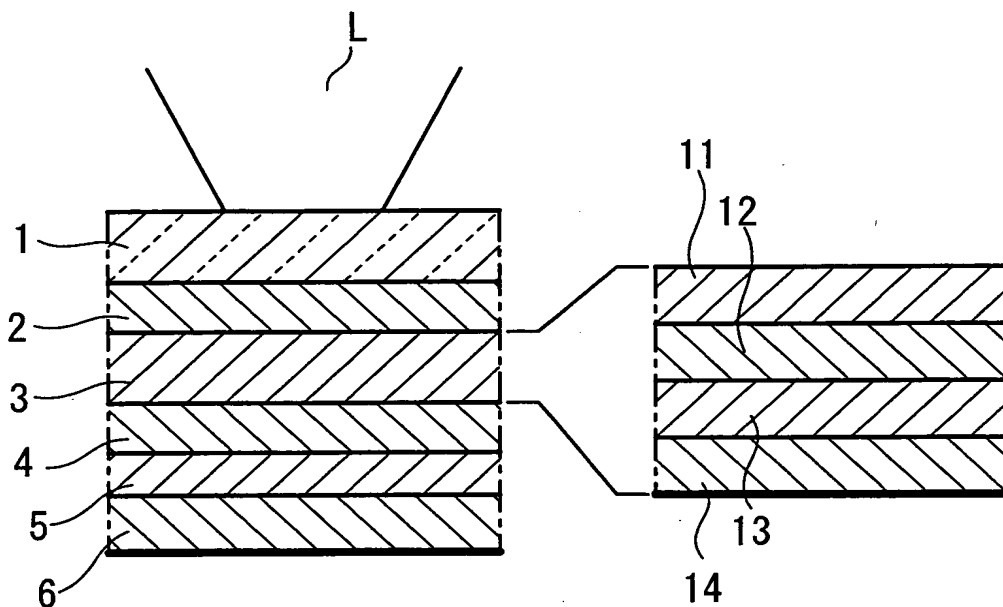
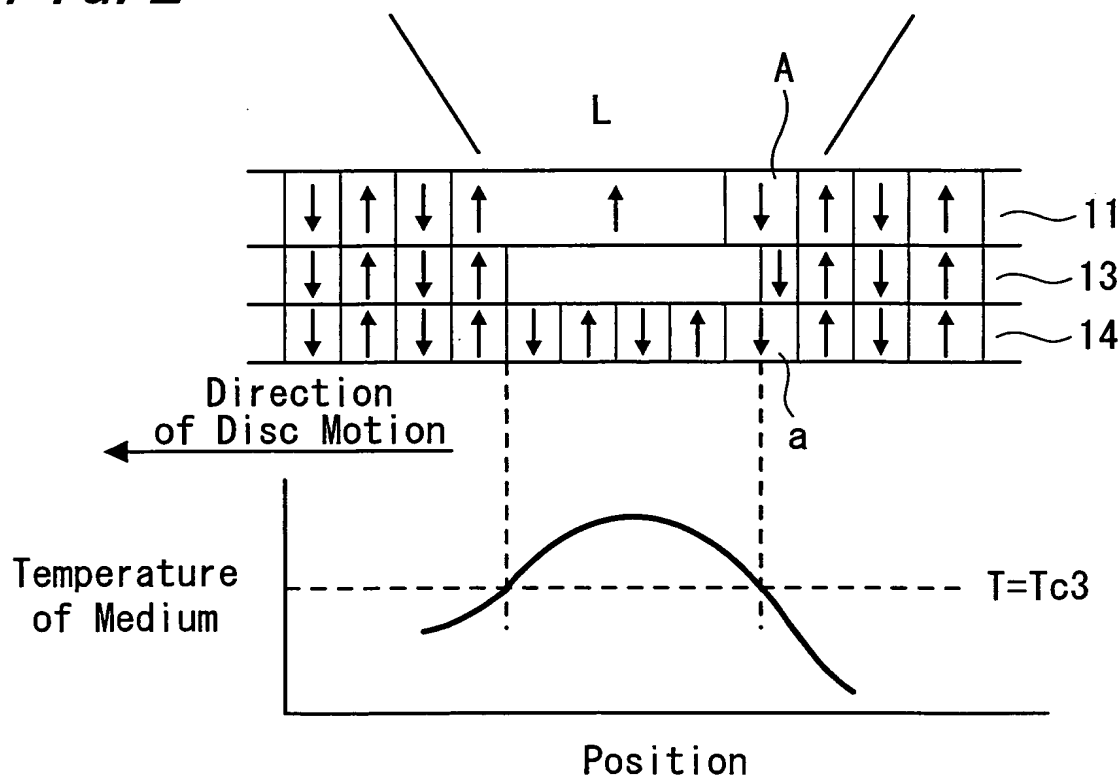


FIG. 2



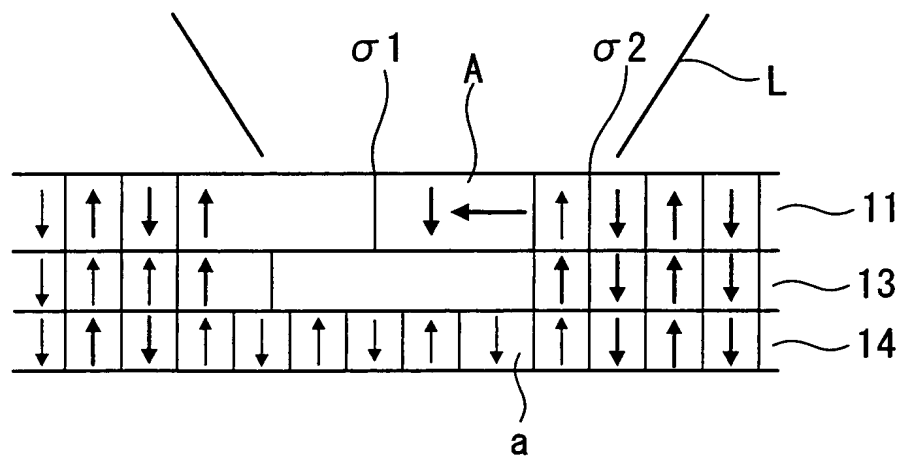


FIG. 6

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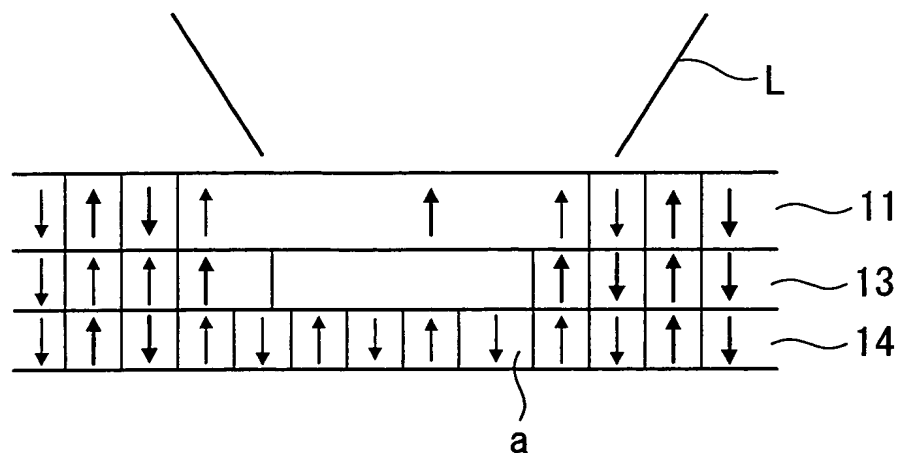


FIG. 7

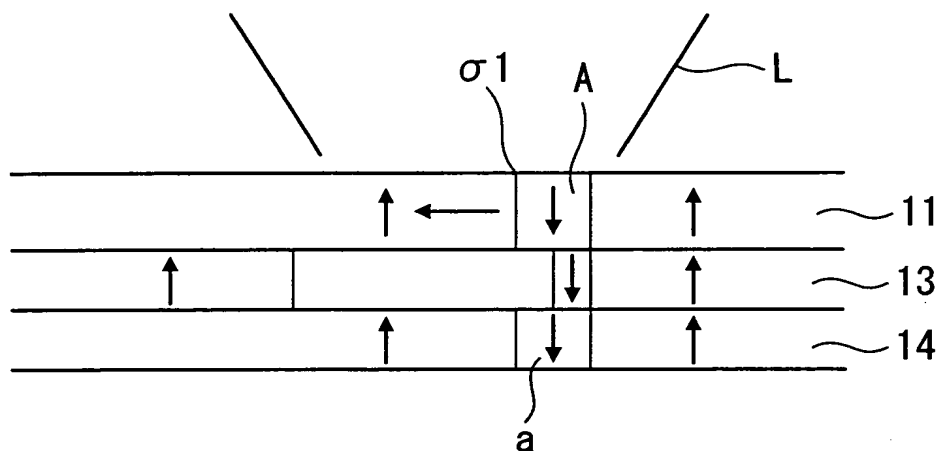
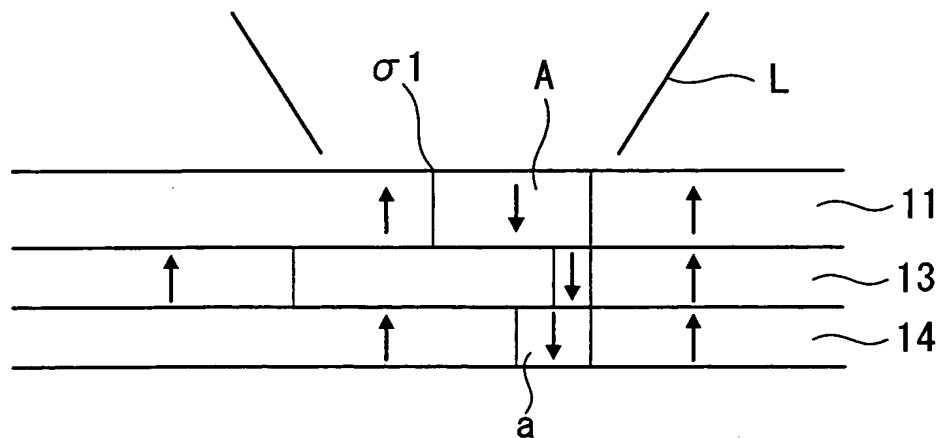


FIG. 8



A cross-sectional view of a semiconductor device. It shows a substrate with a channel region labeled 'a'. Above the channel, there is a gate structure with a top layer labeled 'L'. Arrows indicate the direction of current flow or carrier movement within the channel and the gate region.

Diagram illustrating a cross-sectional view of a semiconductor device. The structure consists of a substrate with multiple layers. A vertical structure, labeled 'a', is formed in the substrate. The structure 'a' includes a hatched region in the middle layer (13) and a region in the bottom layer (14). The top layer is labeled 11. A diagonal line 'LN' is shown on the left, and a diagonal line 'L' is shown on the right. Arrows indicate the direction of light or signal flow: upward arrows are present in layers 11, 13, and 14, and a downward arrow is present in layer 14.

Figure 1 is a schematic diagram of a magnetic head assembly. It shows a cross-section of a head with a gap 'a' and a magnetic layer '11'. A magnetic field 'B' is applied, and a current 'I' flows through a coil. The head is shown in a tilted position relative to a surface 'L'.

FIG. 12

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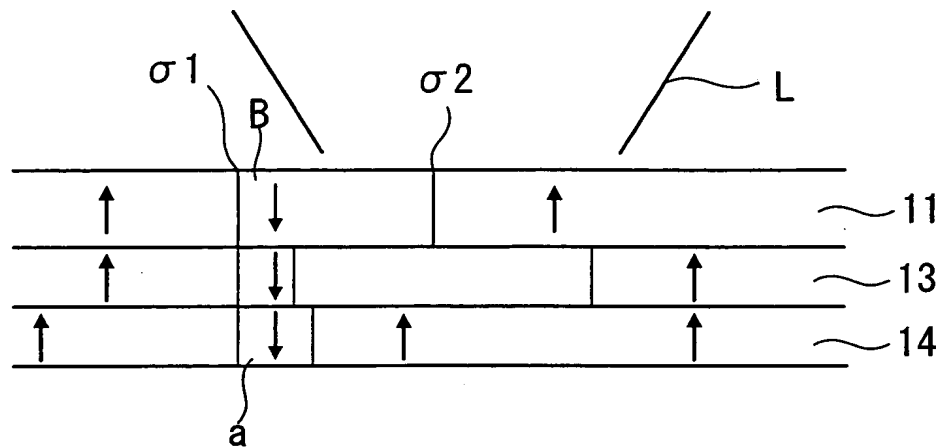


FIG. 13

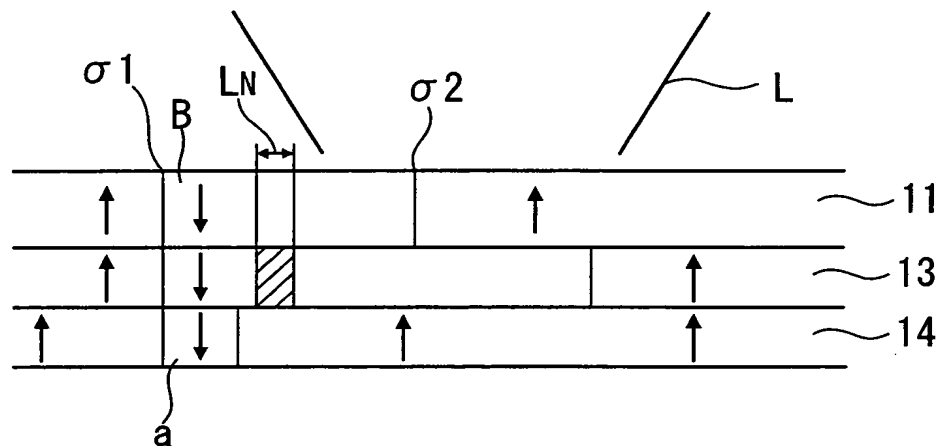
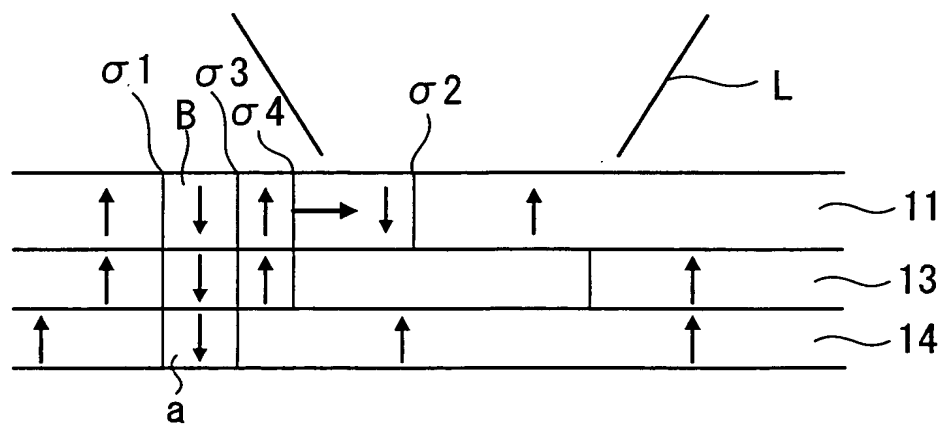


FIG. 14



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FIG. 15

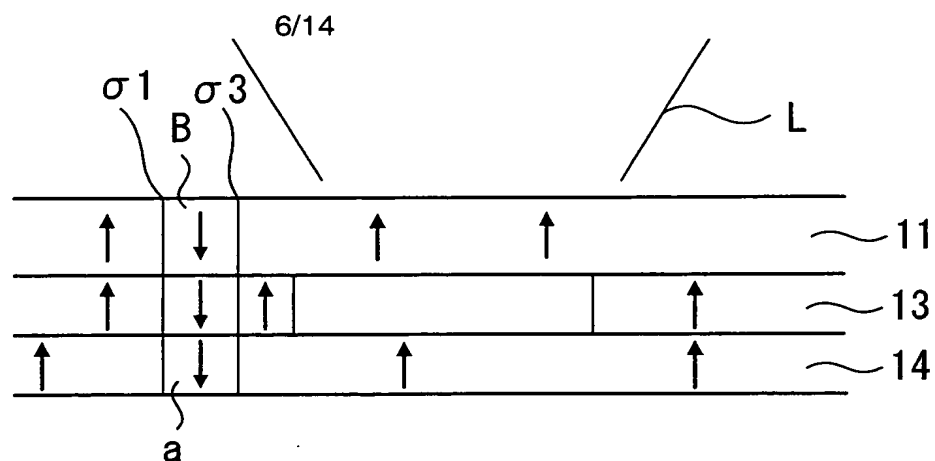


FIG. 16

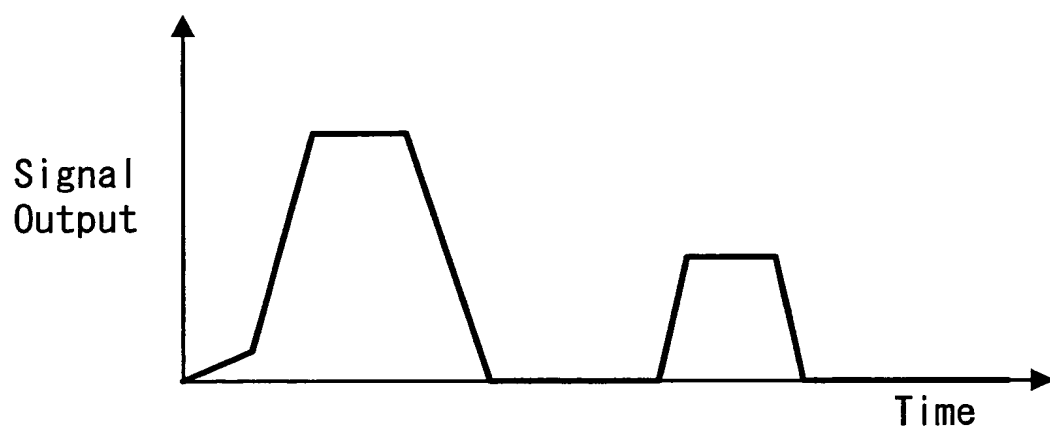
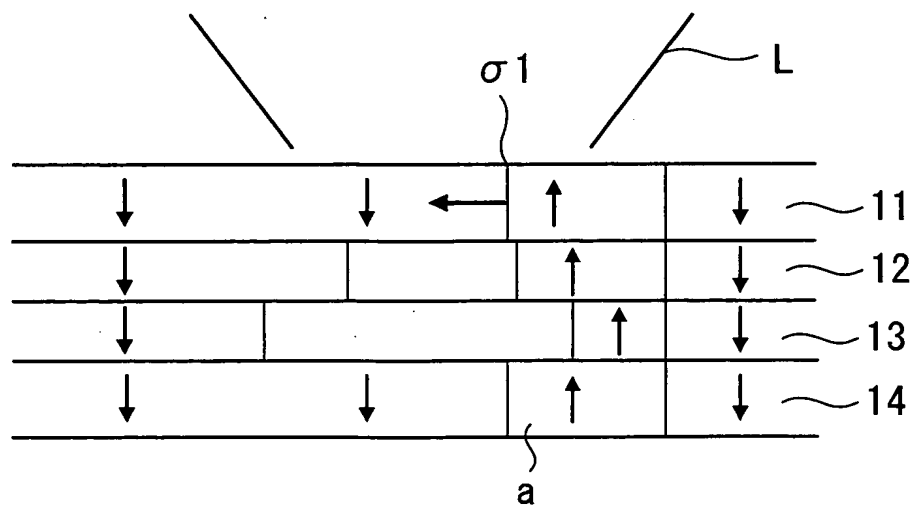


FIG. 17



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FIG. 18

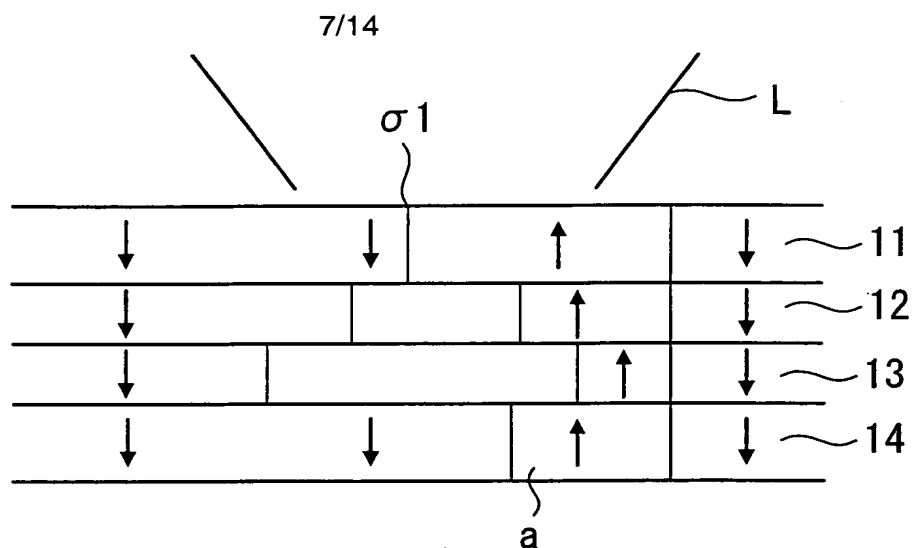


FIG. 19

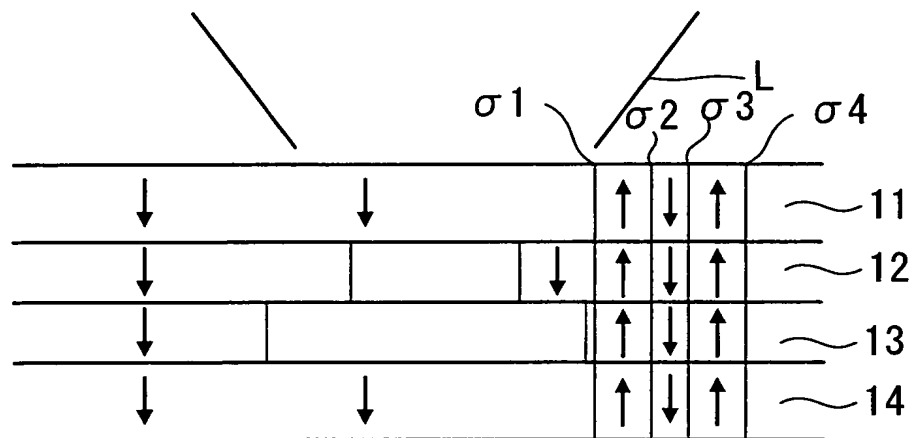
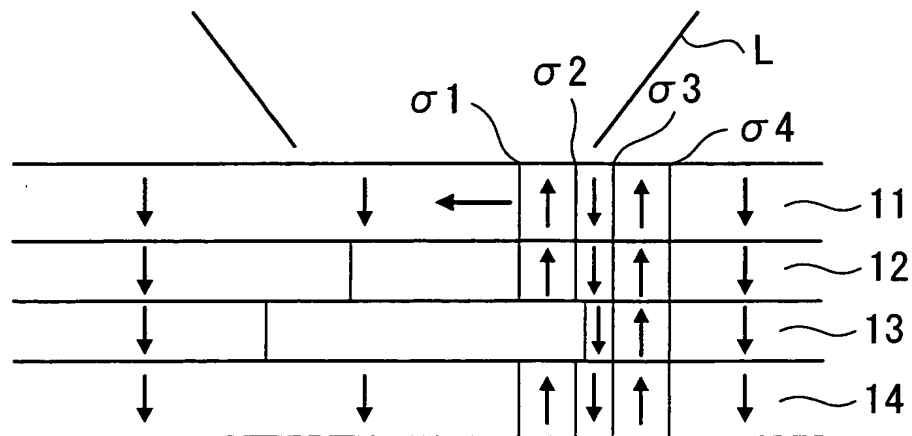


FIG. 20



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FIG. 21

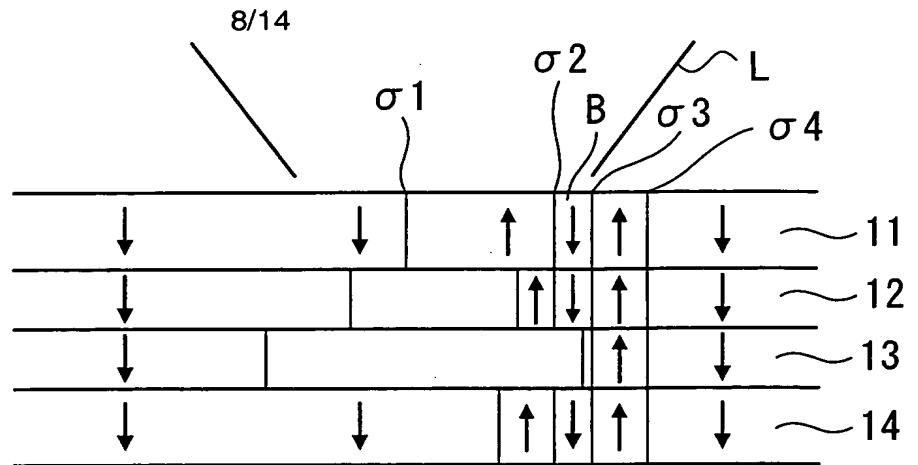


FIG. 22

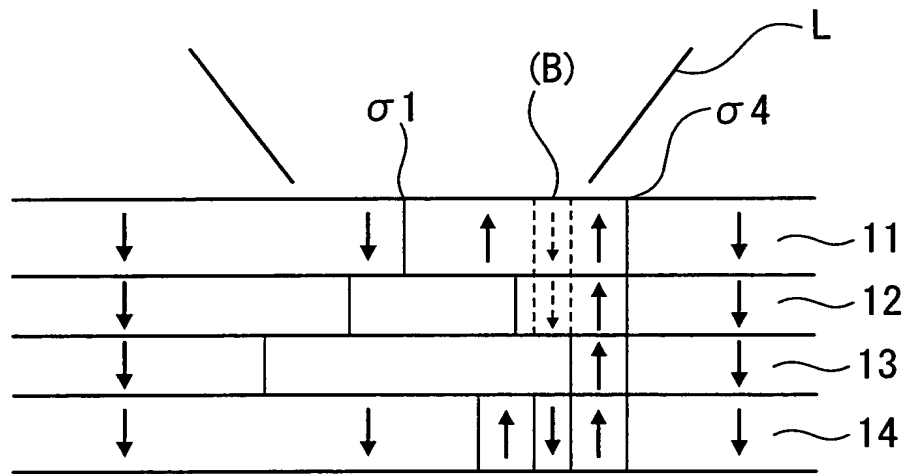
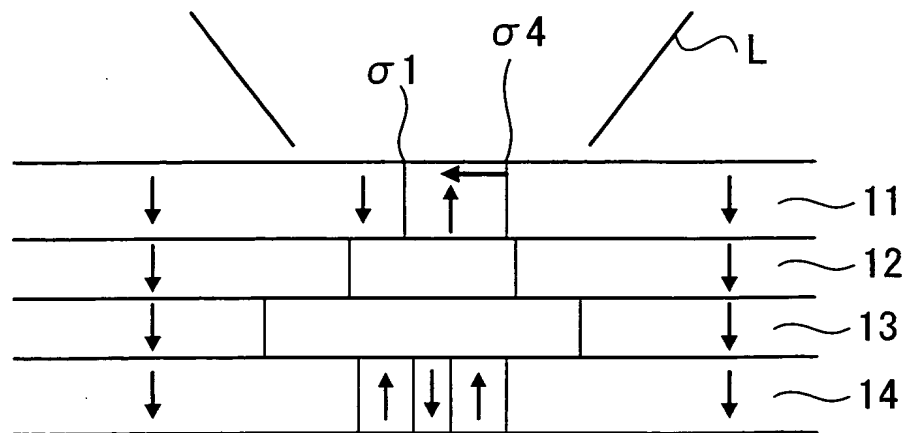


FIG. 23



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FIG. 24A

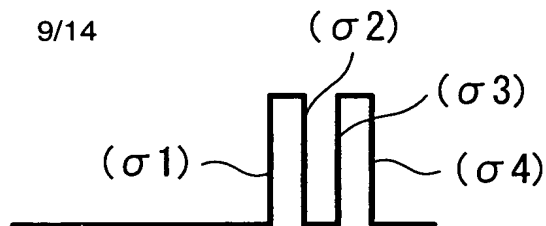


FIG. 24B

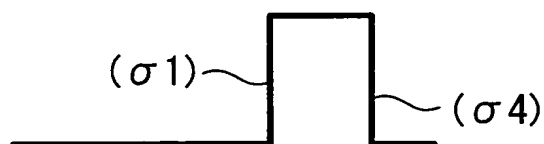


FIG. 25

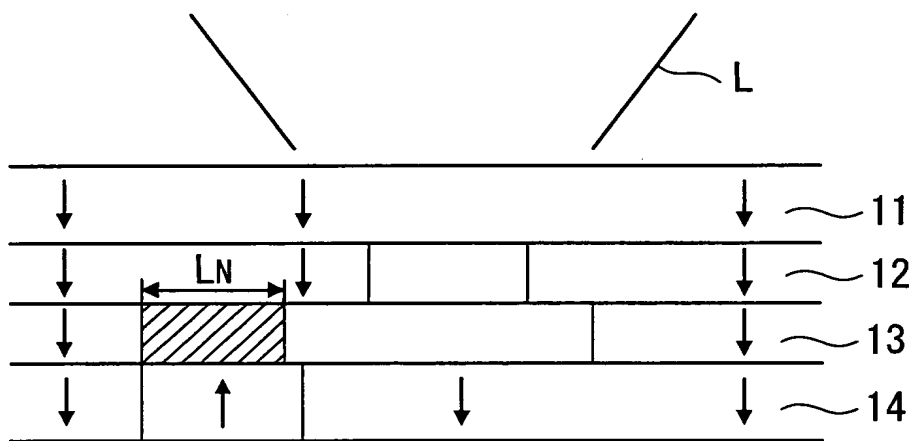
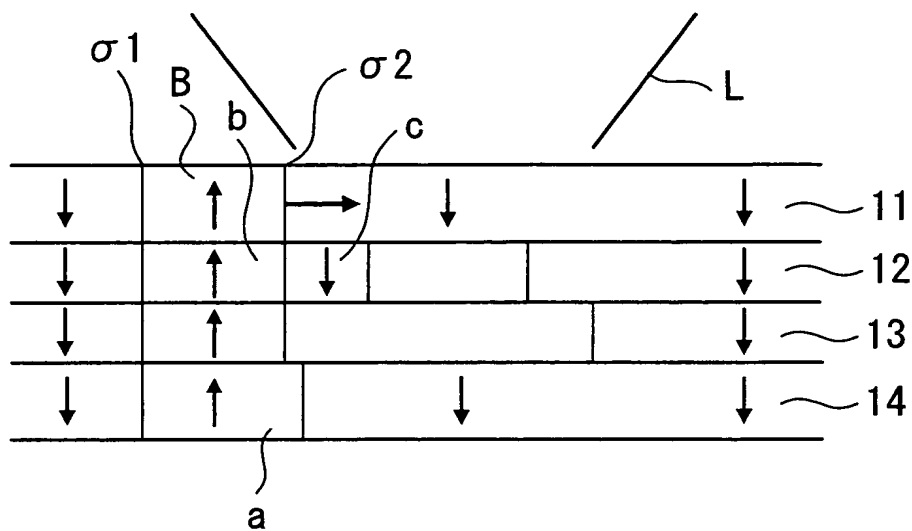


FIG. 26



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FIG. 27

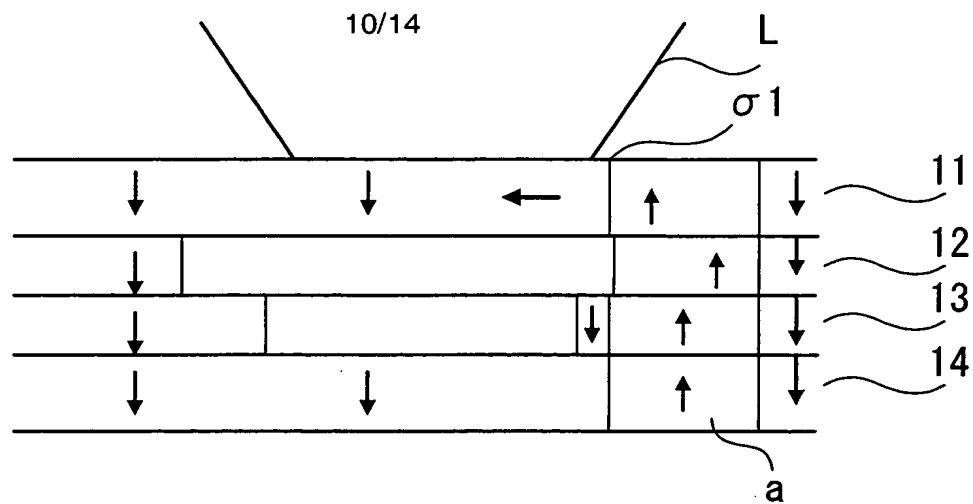


FIG. 28

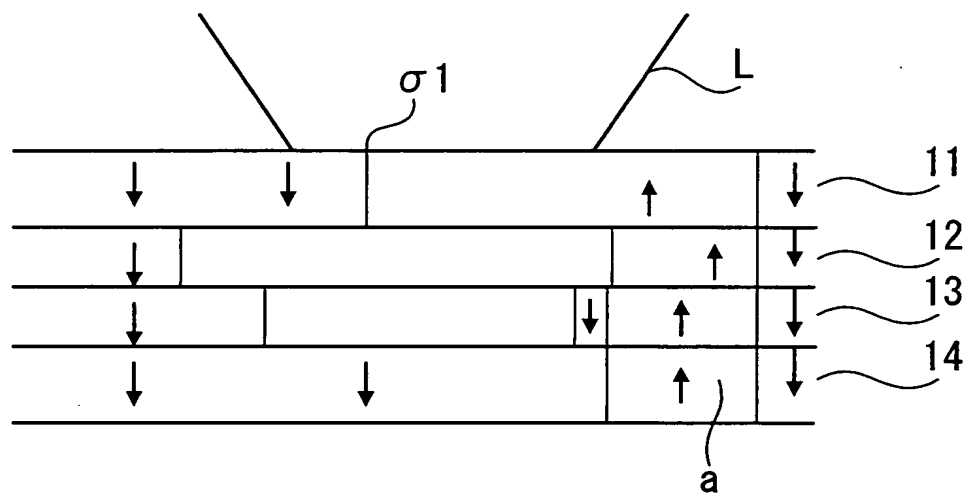
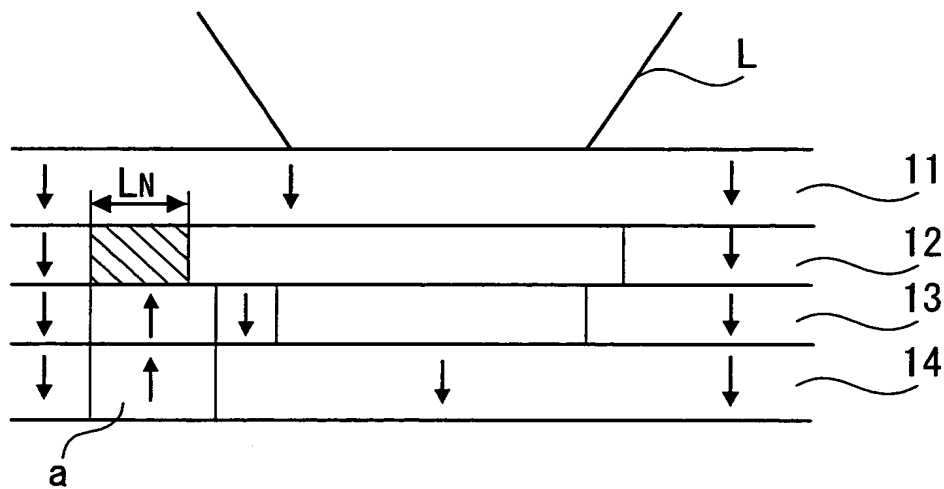


FIG. 29



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FIG. 30

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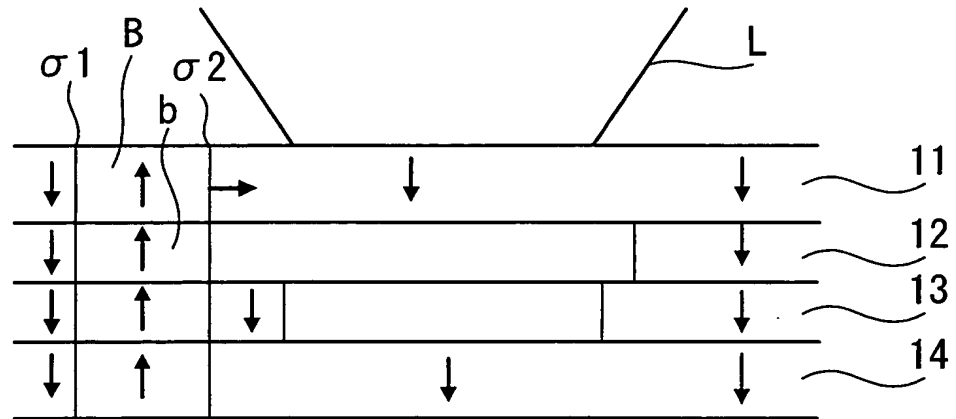


FIG. 31

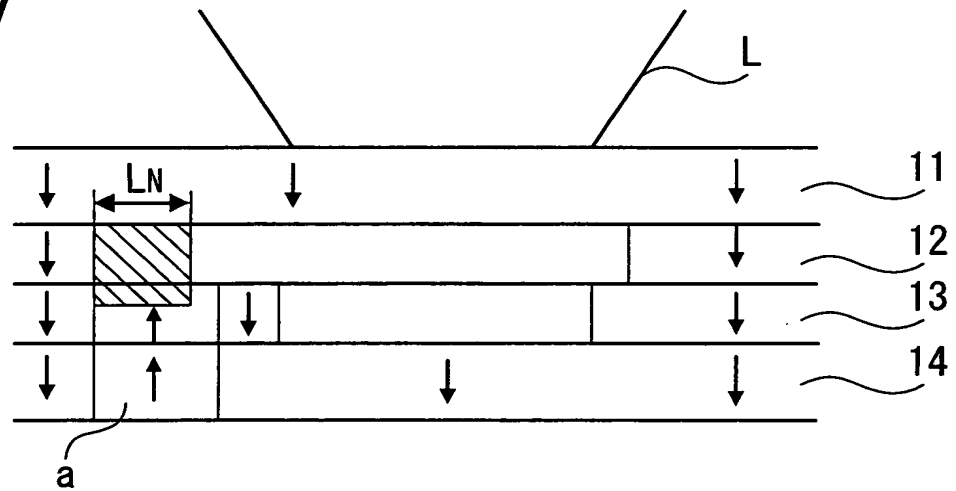
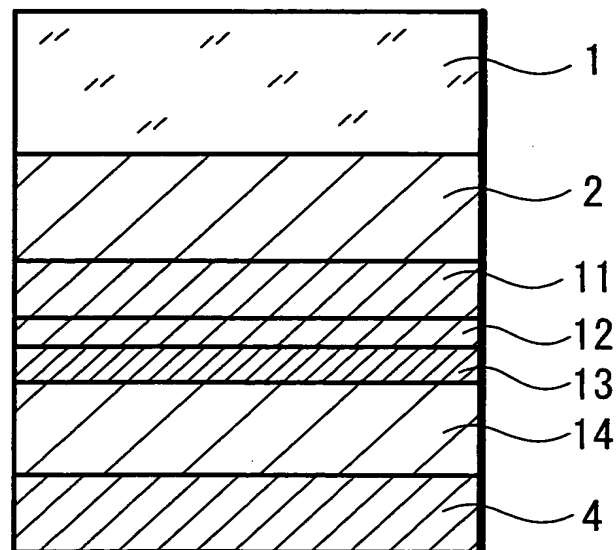


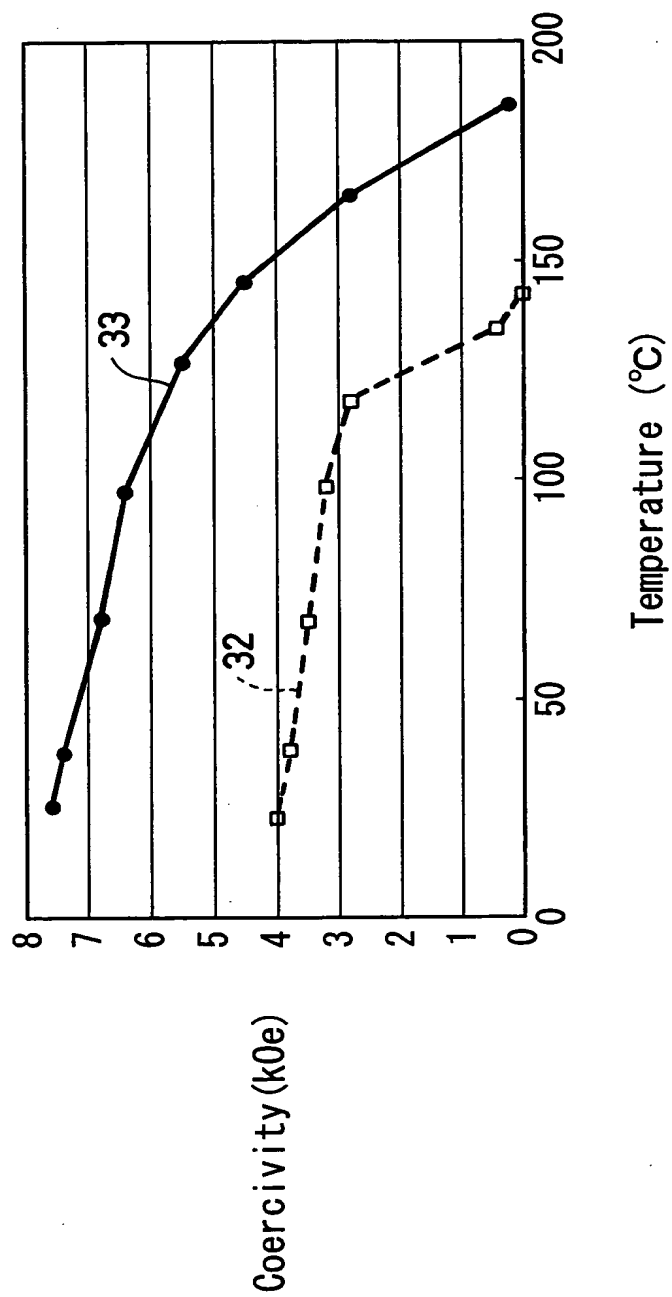
FIG. 32



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FIG. 33



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FIG. 34

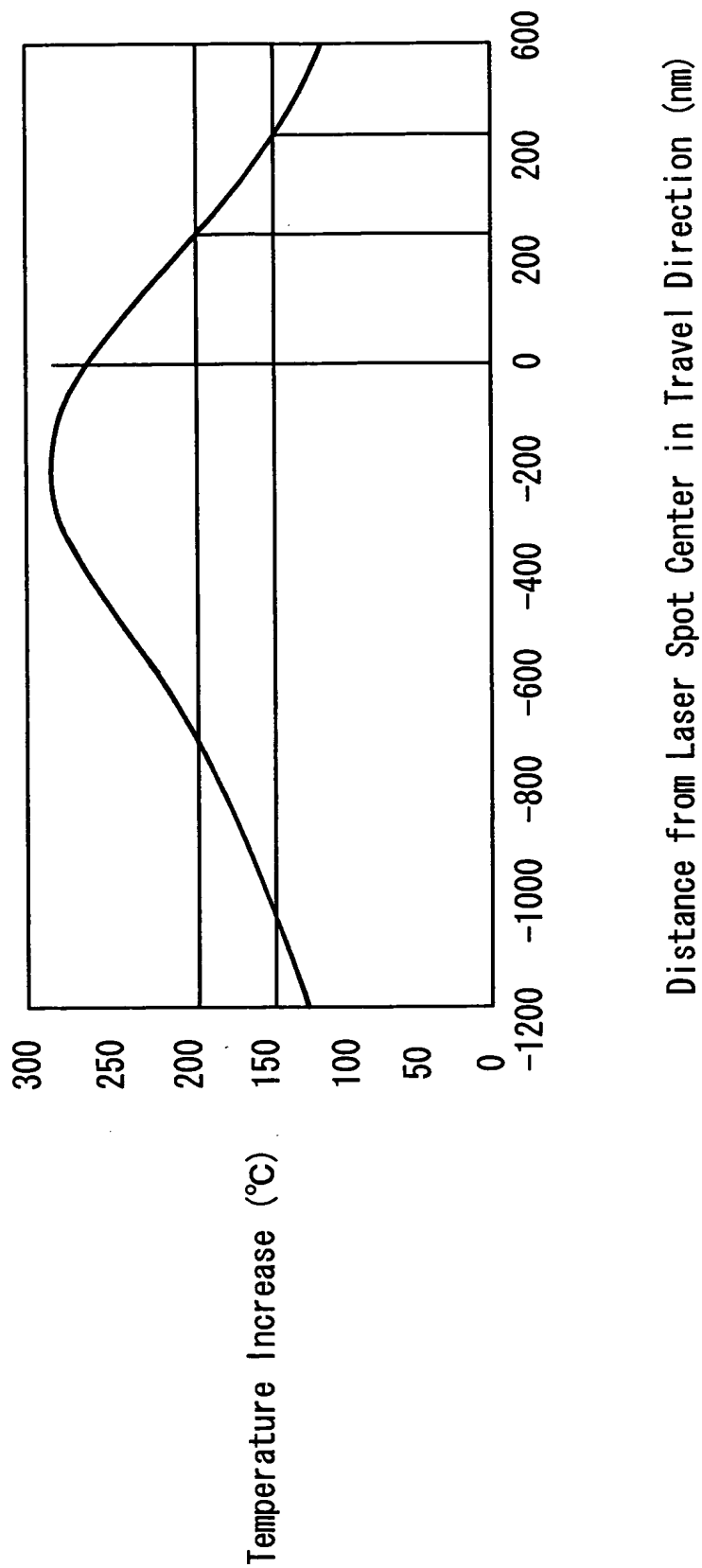


FIG. 35

